ABLUE TECHNOLOGY

PTR9813

Bluetooth 5.2 ready multi-protocol Module
Embedded Cortex™ M4F 32 bit processor
Support Bluetooth Direct Finding AOA/AOD,
Support Zigbee, Thread, MESH, ANT
Ideal choice of IoT and Smart product

The PTR9813 ultra-low power Bluetooth 5 ready multiprotocol System on Module based on the nRF52833 from Nordic Semiconductor. The module can support Bluetooth 5.2 by upgrading the protocol stack. The module with an ARM® Cortex™ M4F 32 bit processor, 512KB Flash/128KB RAM, Bluetooth 5.1 Direct Finding AOA/AOD support， embedded 2.4GHz transceiver, provide a complete solution with no additional RF design, Bluetooth 5, ANT/ANT+, 802.15.4 and 2.4GHz proprietary multiprotocol support, allowing faster time to market, while simplifying designs, reducing BOM costs, also reduce the burden of Regulatory approvals to enter the world market. Making you more quickly into the Bluetooth smart application and remove the worries.

Features

- Nordic nRF52833 with ARM Cortex M4F
- Multiprotocol support : Bluetooth 5.1, ANT/ANT+, and 2.4GHz proprietary, 802.15.4 Thread and Zigbee, .
- Bluetooth 5.1 Direction Finding AOA/AOD
- Bluetooth 5: 2 /1Mbps, 500 kbps, 125 kbps
- IEEE 802.15.4-2006: 250 kbps
- Proprietary 2.4 GHz: 2 Mbps, 1 Mbps
- Integrated DC-DC converter
- Serial Wire Debug (SWD)
- Nordic SoftDevice Ready
- Over-the-Air (OTA) firmware update
- Flash/RAM: 512KB/128KB.
- 40 General purpose I/O pins
- 15 level low-power comparator with wake-up from System OFF mode
- Two 2-wire Master/Slave (I2C compatible)
- I2S audio interface
- 12 bit/200KSPS ADC
- High-speed 32 MHz SPI
- 4 SPI Master/ 3 SPI Slave)
- 2 UART (with CTS/RTS and DMA)
- 4x 4-channel PWM unit with EasyDMA
- USB 2.0 full speed (12 Mbps) controller
- 20 channel CPU independent Programmable Peripheral Interconnect (PPI).
- Quadrature Demodulator (QDEC)
- 128-bit AES HW encryption
- 5 x 32 bits timers, 3 xReal Time Counters (RTC)
- NFC-A tag interface for OOB pairing
- TX power: +8dBm to-20dBm in 4 dB steps.
- PCB antenna or IPX Antenna
- Sizes: 24.3x17.5 x1.8mm
- DC/DC on board
- No external components required
- Operation voltage: 1.7V to 5.5V

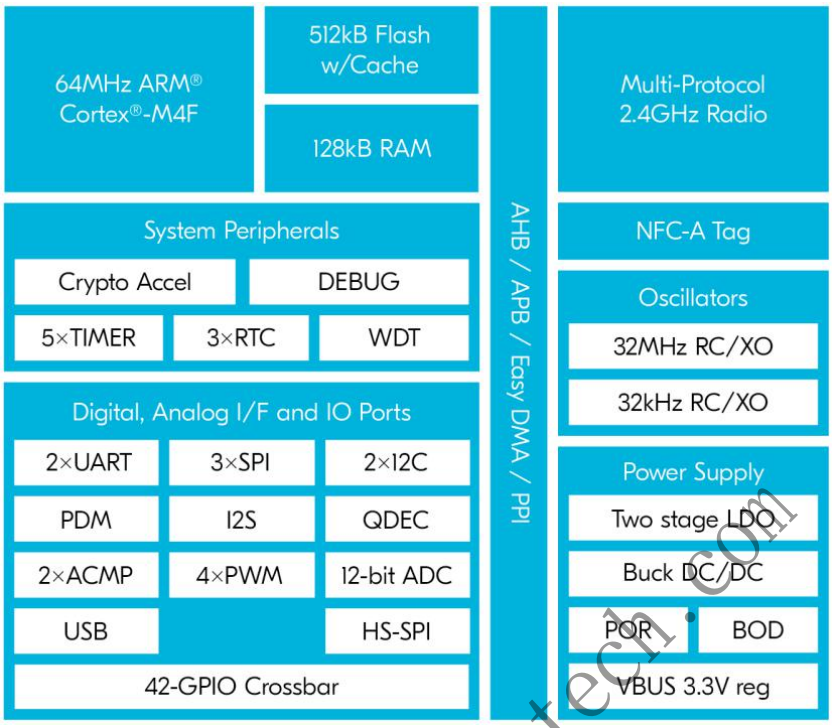
Typical Applications:

- - 2.4 GHz Bluetooth low energy systems
- - Proprietary 2.4 GHz systems
- - Sports and leisure equipment
- - Mobile phone accessories, Connected Appliances
- - Health Care and Medical
- - Consumer Electronics, Game pads
- - Human Interface Devices, Remote control
- - Building environment control / monitoring
- - RFID, Security Applications，Low-Power Sensors
- - Bluetooth Low Energy GateWay
- - iBeacons™, Eddystone™, Indoor navigation
- - Lighting Products
- - Fitness devices, Wearables

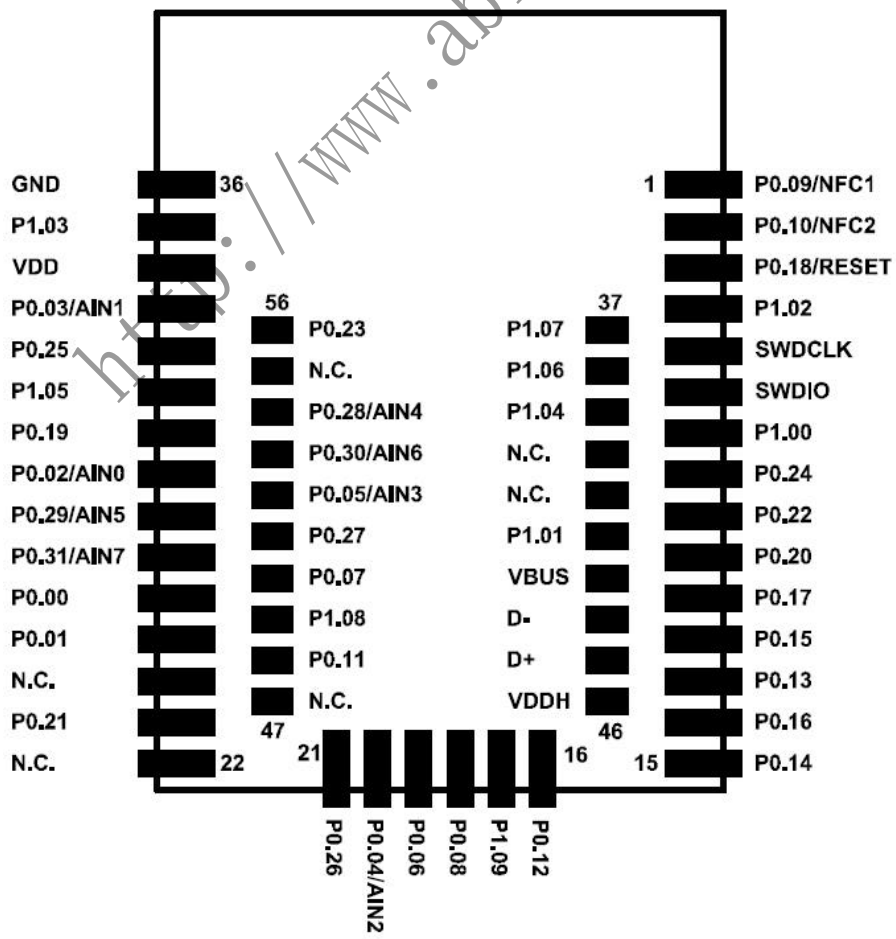
Quick Specifications:

Multi-protocol	
Version	Bluetooth 5.1 and Higher/ANT/2.4GHz Proprietary/802.15.4/Zigbee
Security	AES-128
Radio	
Frequency	2.360GHz to 2.500GHz
Modulations	GFSK at 2/1 Mbps, Long range 125/500kbps, 802.15.4- 250 kbps
Transmit power	+8dBm to -20dBm
Receiver sensitivity	-103dBm@BLE 125kbps(long range), -96dBm@BLE 1M
Antenna	Integrated PCB Antenna / Ext. IPX Antenna
Current Consumption	
TX only @ +8 dBm, @ 3V, DC/DC enabled	14.2 mA
TX only @ 0 dBm, @ 3V, DC/DC enabled	4.9 mA
RX only @ 1 Mbps @ 3V, DC/DC enabled	4.8 mA
CPU @ 64MHz from flash @ 3V, DC/DC	3.3 mA
System On	1.5 μA
System Off	0.6μA
Operating conditions	
Power supply	1.7~5.5V
Operating temperature	-25~+85 °C

Block diagram:



Pin Description of Module（Top View）:



Pin	Name	Description	Recommend usage
Pin1	P0.09/NFC1	Digital I/O/ NFC input	Standard drive, low frequency I/O
Pin2	P0.10/NFC2	Digital I/O/ NFC input	Standard drive, low frequency I/O
Pin3	P0.18/RESET	Digital I/O/RESET	
Pin4	P1.02	Digital I/O	Standard drive, low frequency I/O
Pin5	SWDCLK	HW debug and programming	
Pin6	SWDIO	HW debug and programming	
Pin7	P1.00	Digital I/O	
Pin8	P0.24	Digital I/O	
Pin9	P0.22	Digital I/O	
Pin10	P0.20	Digital I/O	
Pin11	P0.17	Digital I/O	
Pin12	P0.15	Digital I/O	
Pin13	P0.13	Digital I/O	
Pin14	P0.16	Digital I/O	
Pin15	P0.14	Digital I/O	
Pin16	P0.12	Digital I/O	
Pin17	P1.09	Digital I/O	
Pin18	P0.08	Digital I/O	
Pin19	P0.06	Digital I/O	
Pin20	P0.04/AIN2	Digital I/O/Analog input 2	
Pin21	P0.26	Digital I/O	
Pin22	NC	NC	
Pin23	P0.21	Digital I/O	
Pin24	NC	NC	
Pin25	P0.01/XL2	Reserve for 32.768KHz use	
Pin26	P0.00/XL1	Reserve for 32.768KHz use	
Pin27	P0.31/AIN7	Digital I/O/Analog input 7	Standard drive, low frequency I/O
Pin28	P0.29/AIN5	Digital I/O/Analog input 5	Standard drive, low frequency I/O
Pin29	P0.02/AIN0	Digital I/O/Analog input 0	Standard drive, low frequency I/O
Pin30	P0.19	Digital I/O	Standard drive, low frequency I/O
Pin31	P1.05	Digital I/O	Standard drive, low frequency I/O
Pin32	P0.25	Digital I/O	Standard drive, low frequency I/O
Pin33	P0.03/AIN1	Digital I/O/Analog input 1	Standard drive, low frequency I/O
Pin34	VDD	Power Supply	
Pin35	P1.03	Digital I/O	Standard drive, low frequency I/O
Pin36	GND	Ground	
Pin37	P1.07	Digital I/O	Standard drive, low frequency I/O
Pin38	P1.06	Digital I/O	Standard drive, low frequency I/O
Pin39	P1.04	Digital I/O	Standard drive, low frequency I/O
Pin40	NC	NC	
Pin41	NC	NC	
Pin42	P1.01	Digital I/O	Standard drive, low frequency I/O

Pin43	VBUS	Power	5 V input for USB 3.3 V regulator
Pin44	D-	USB D-	USB
Pin45	D+	USB D+	USB
Pin46	VDDH	High voltage power supply	
Pin47	NC	NC	
Pin48	P0.11	Digital I/O	
Pin49	P1.08	Digital I/O	
Pin50	P0.07	Digital I/O	
Pin51	P0.27	Digital I/O	
Pin52	P0.05/AIN3	Digital I/O/Analog input 3	
Pin53	P0.30/AIN6	Digital I/O/Analog input 6	Standard drive, low frequency I/O
Pin54	P0.28/AIN4	Digital I/O/Analog input 4	Standard drive, low frequency I/O
Pin55	NC	NC	
Pin56	P0.23	Digital I/O	Standard drive, low frequency I/O

*Low frequency I/O is signals with a frequency up to 10 kHz

Note: An internal 4.7μF bulk capacitor has been included on the module. For those application that with heavy GPIO usage and/or current draw, it is good design practice to add additional bulk capacitance as required for your application.

General Purpose I/O:

Each GPIO can be accessed individually with the following user configurable features:

- Input/output direction
- Output drive strength
- Internal pull-up and pull-down resistors
- Wake-up from high or low level triggers on all pins
- Trigger interrupt on all pins
- All pins can be used by the PPI task/event system; the maximum number of pins that can be interfaced through the PPI at the same time is limited by the number of GPIOTE channels
- All pins can be individually configured to carry serial interface or quadrature demodulator signals

Hardware RESET:

There is on-chip power-on reset circuitry, But can still be used in external reset mode, in this case, GPIO pin P0.18 as an external hardware reset pin. In order to utilize P0.18 as a hardware reset, the UICR registers PSELRESET[0] and PSELRESET[1] must be set alike, to the value of 0x7FFFFF12. When P0.18 is programmed as RESET, the internal pull-up is automatically enabled.

HW debug and flash programming of Module :

The Module support the two pin Serial Wire Debug (SWD) interface and offers flexible and powerful mechanism for non-intrusive debugging of program code. Breakpoints, single stepping, and instruction trace capture of code execution flow are part of this support.

Pin	Flash Program interface
SWDIO	Debug and flash programming I/O
SWDCLK	Debug and flash programming I/O

This is the hardware debug and flash programming of module, J-Link Lite support, please refer www.segger.com.

Power and Configuration:

The module has two internal regulator stages. REG1 regulator stage has the regulator type options of Low-dropout regulator (LDO) and Buck regulator (DC/DC). REG0 regulator stage has only the option of Low-dropout regulator (LDO). The first regulator, REG0, is fed by the VDDH pin and can accept a source voltage of 2.5 V to 5.5 V. The output of REG0 is connected to the VDD pin and the input of the second regulator stage REG1. REG1 supplies power to the module core and can accept an input source voltage of 1.7V to 3.6V. Depending on how the VDD and VDDH pins are connected, the module will operate in one of two modes: Normal/Low Voltage (LV) or High Voltage (HV). The voltage present on the VDD pin is always the GPIO high logic level voltage, regardless of power mode.

To enter LV Mode, the same source voltage is applied to both the VDD and VDDH pins causing REG0 to automatically shut down leaving only the REG1 stage active. To enter HV, the source voltage is only applied to VDDH causing the VDD pin to become an output source supplied by REG0.

Mode	Pin of Module	Name	Power Connection
Normal/Low Voltage (LV)	Pin 34	VDD	1.7V to 3.6V source in
	Pin 46	VDDH	Same source as VDD
High Voltage (HV)	Pin 34	VDD	1.8V to 3.3V supply out
	Pin 46	VDDH	2.5V to 5.5V source in

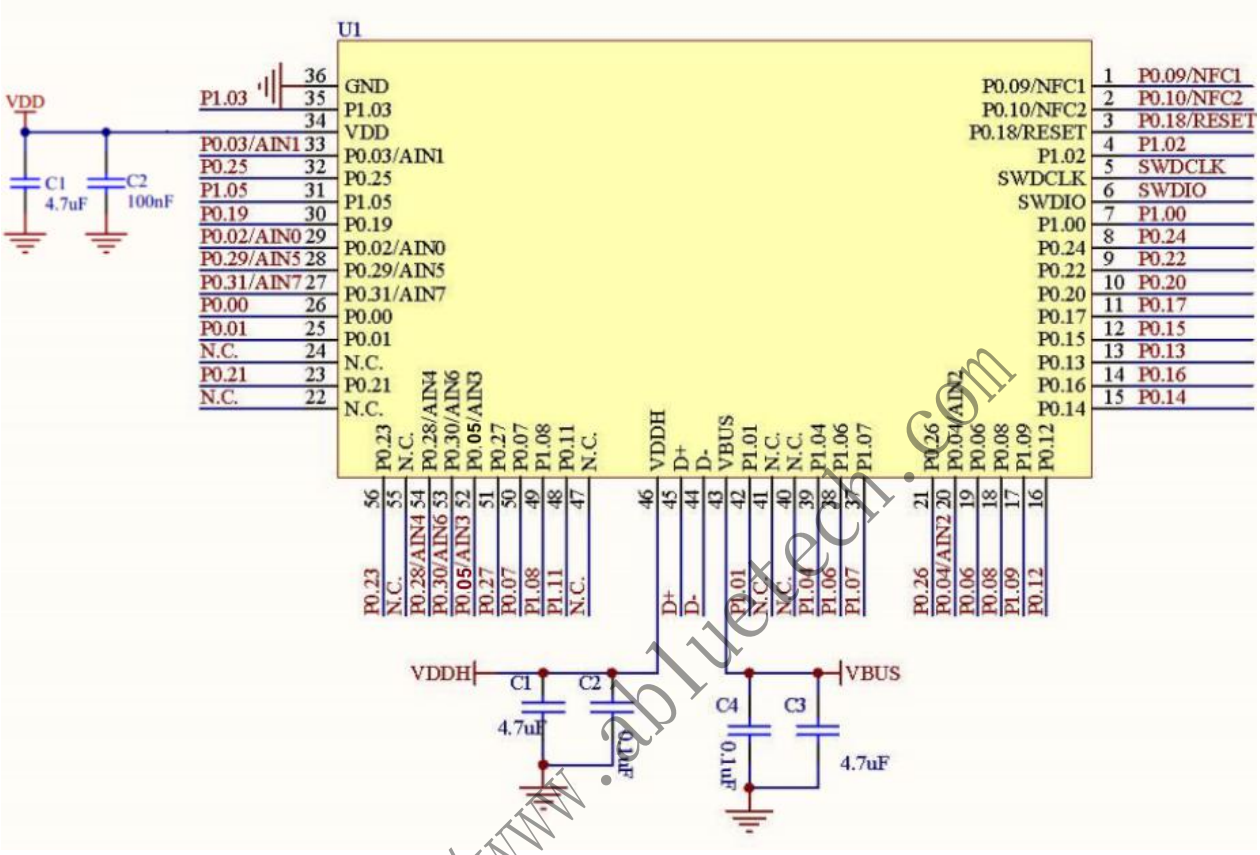
Important: In HV mode, the GPIO high voltage defaults to 1.8V (configurable by REGOUT0 register). In order to ensure that the voltage on the GPIO pins match each other when the module communicates with external devices, attention should be paid to the power supply of external devices: 1) The power supply voltage of the external equipment connected to the module should be consistent with the VDD of the module; 2) When the power supply voltage of the external equipment is not consistent with the VDD of the module, a conversion circuit should be used.

USB Power: The USB interface on the Module can be used when the module is in either Normal /Low Voltage (LV) or High Voltage (HV) mode. The Module USB PHY is powered by a dedicated, internal LDO regulator that is fed by the VBUS pin (Pin43). This means that applying power to only the VBUS pin will not power the rest of the module. In order for the USB PHY to operate, VBUS must be externally powered.

Reference circuitry:

In this section there are 5 reference circuits to show how to design an application circuit with this module.

Reference Circuit configuration 1



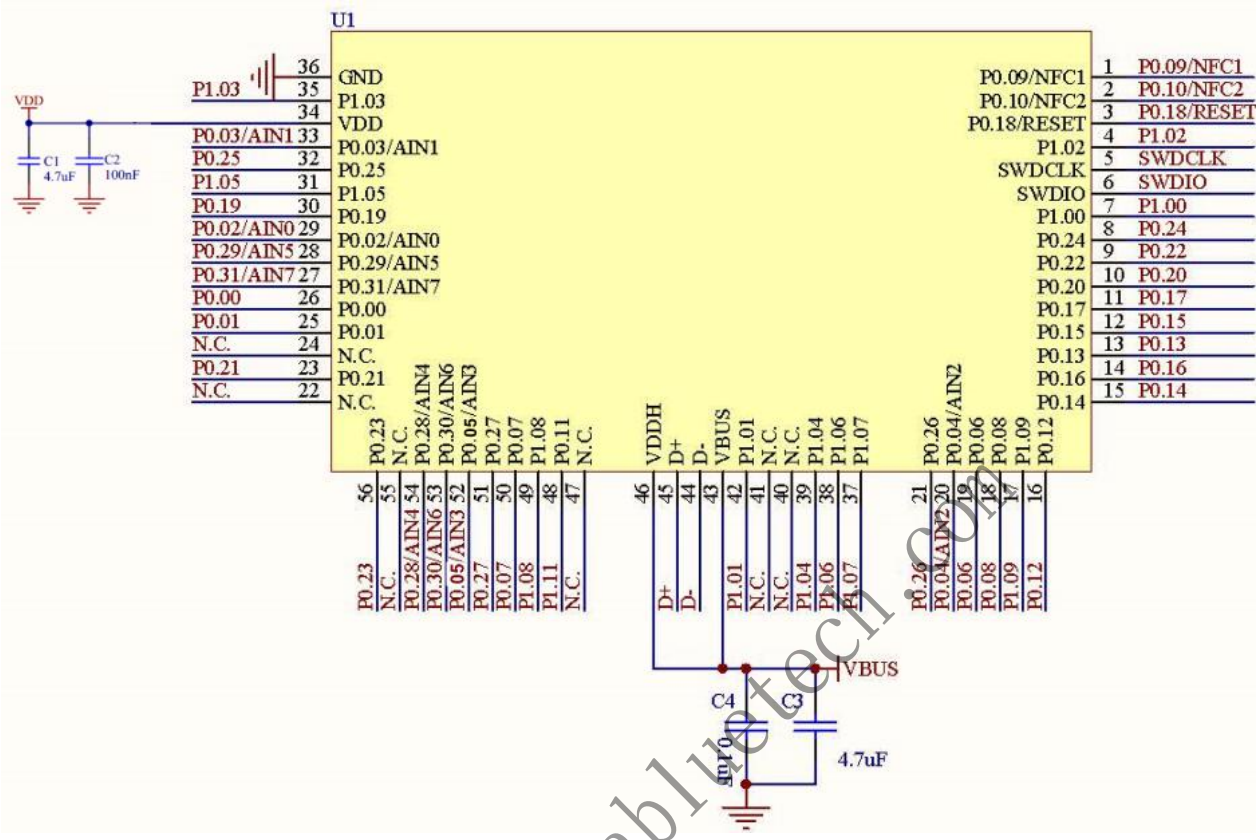
Configurations summary for reference circuit 1

Config no.	Main Supply		USB
	VDDH	VDD	
Config.1	Battery/Ext.regulator	N/A	Yes

Explanation of symbols in reference circuit 1 schematic

Symbol	Parameter	Min.	Typ.	Max.	Units
V_{DDH}	Main supply voltage in high voltage mode	2.5	3.7	5.5	V
V_{BUS}	Supply voltage on VBUS pin	4.35	5	5.5	V

Reference Circuit configuration 2



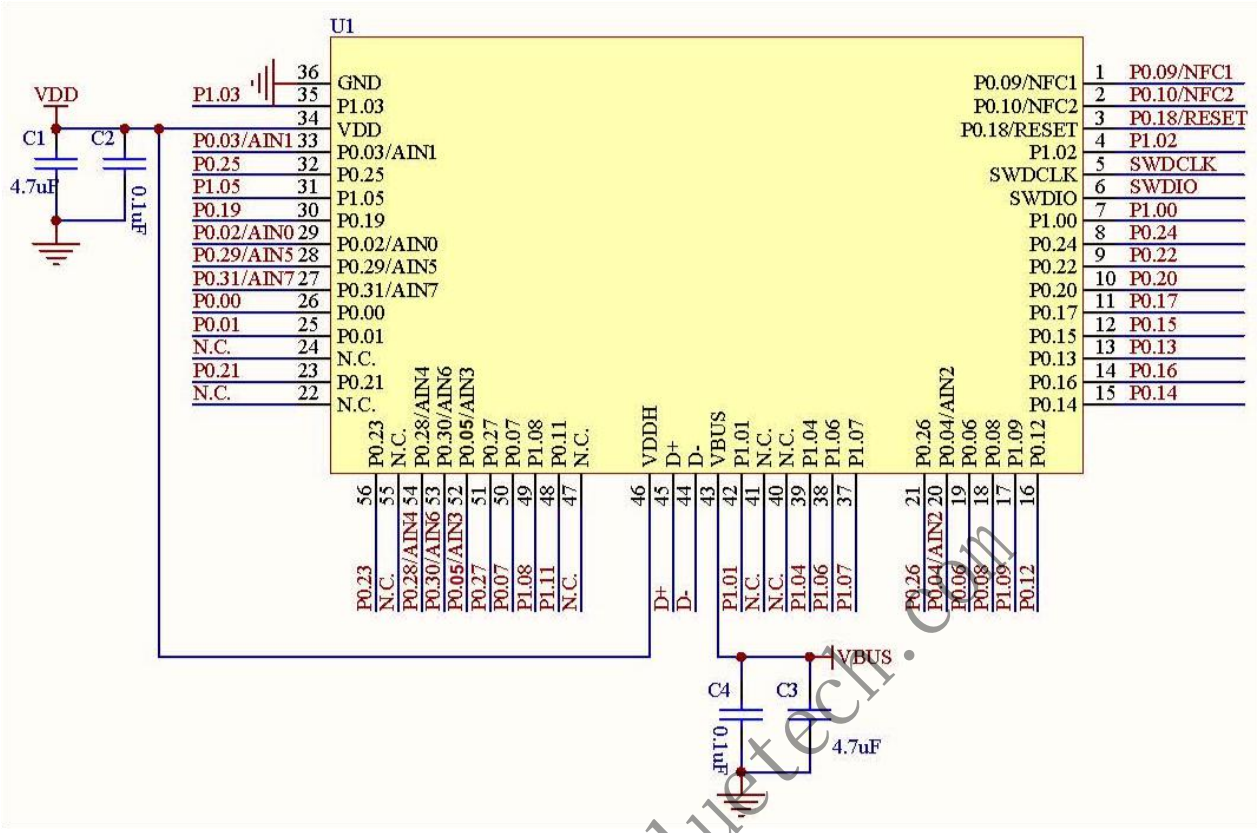
Configurations summary for reference circuit 2

Config no.	Main Supply		USB
	VDDH	VDD	
Config.2	USB(VDDH = VBUS)	N/A	Yes

Explanation of symbols in reference circuit 2 schematic

Symbol	Parameter	Min.	Typ.	Max.	Units
V_{BUS}	Supply voltage on VBUS pin	4.35	5	5.5	V

Reference Circuit configuration 3



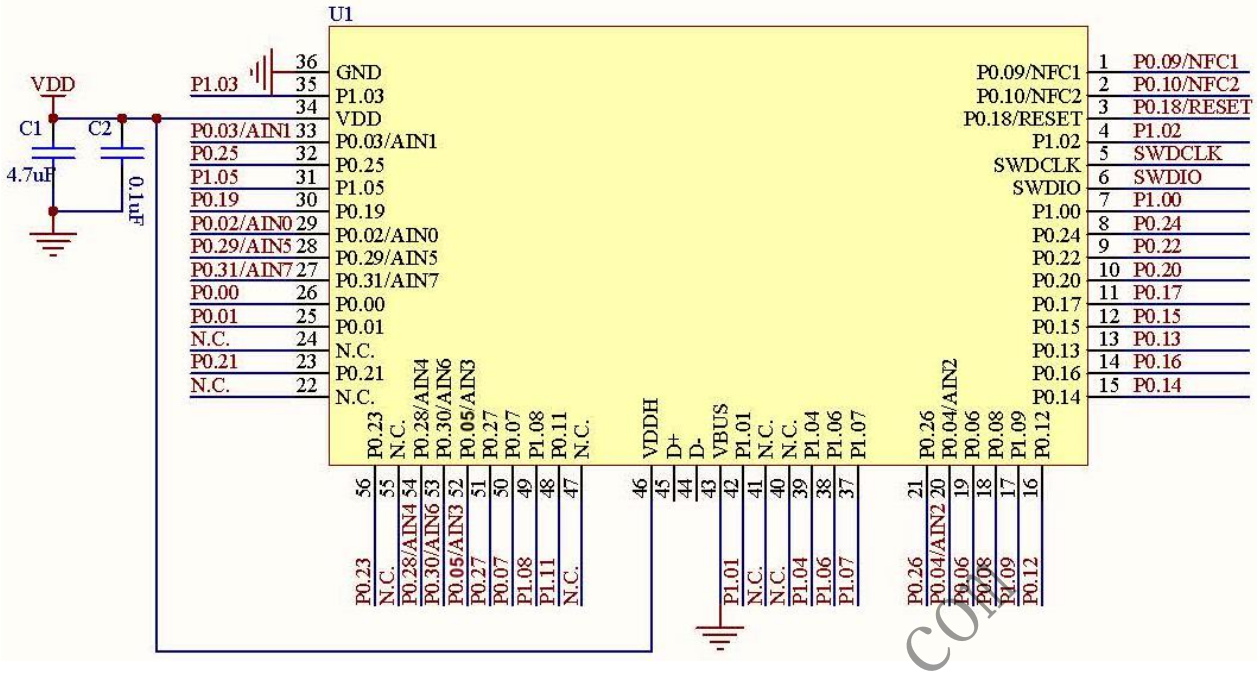
Configurations summary for reference circuit 3

Config no.	Main Supply		USB
	VDDH	VDD	
Config.3	N/A	Battery/Ext.regulator	Yes

Explanation of symbols in reference circuit 3 schematic

Symbol	Parameter	Min.	Typ.	Max.	Units
V_{DD}	Main supply voltage in normal voltage mode	1.7	3	3.6	V
V_{BUS}	Supply voltage on VBUS pin	4.35	5	5.5	V

Reference Circuit configuration 4



Configurations summary for reference circuit 4

Config no.	Main Supply		USB
	VDDH	VDD	
Config.4	N/A	Battery/Ext.regulator	No

Explanation of symbols in reference circuit 4 schematic

Symbol	Parameter	Min.	Typ.	Max.	Units
V_{DD}	Main supply voltage in normal voltage mode	1.7	3	3.6	V

Some general guidance is summarized here:

- Main supply voltage is connected to VDD/VDDH. The system will enter one of two supply voltage

modes, normal or high voltage mode, depending on how the supply voltage is connected to these pins. Normal voltage mode is entered when the supply voltage is connected to both the VDD and VDDH pins (so that VDD equals VDDH).High voltage mode is entered when the supply voltage is only connected to the VDDH pin and the VDD pin is not connected to any voltage supply.

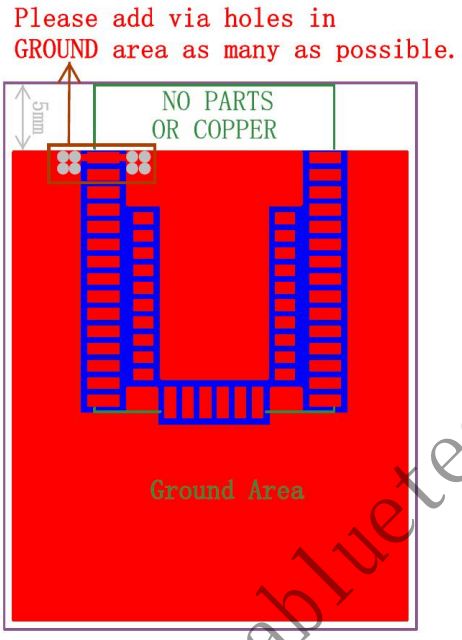
- By default, the LDO regulators in the chip are enabled and the DC/DC regulator of REG1 stage is disabled. Register DCDCEN is used to enable the DC/DC regulator for REG1 stage. External LC filters has been connected in the Module for DC/DC regulators being used.

- The GPIO high reference voltage always equals the level on the VDD pin. In normal voltage mode, the GPIO high level equals the voltage supplied to the VDD pin, and in high voltage mode it equals the level specified in the register REGOUT0 UICR registers.The output voltage range is 1.8~ 3.3V ， the default value is 1.8V.

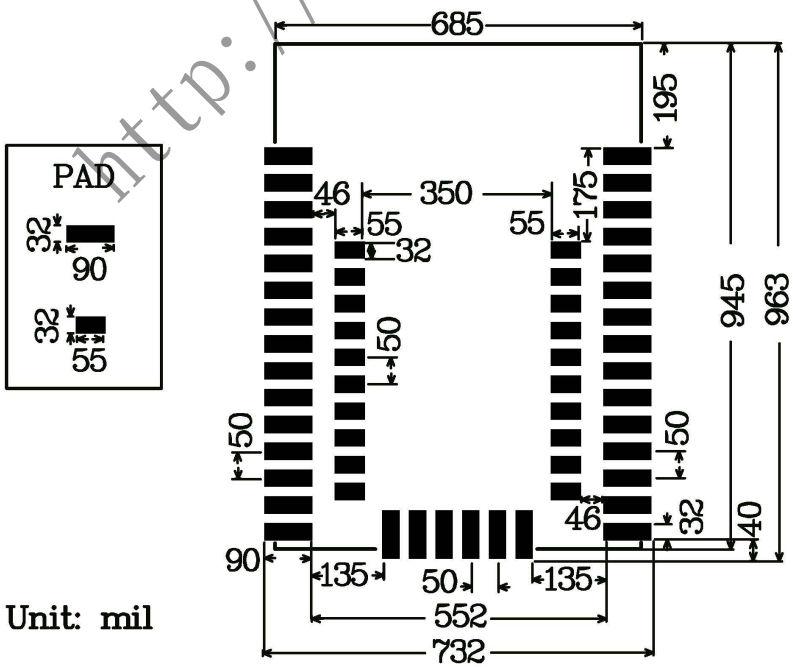
- When using the USB peripheral, 5V USB supply needs to be provided on the VBUS pin.

Recommended RF Layout & Ground Plane:

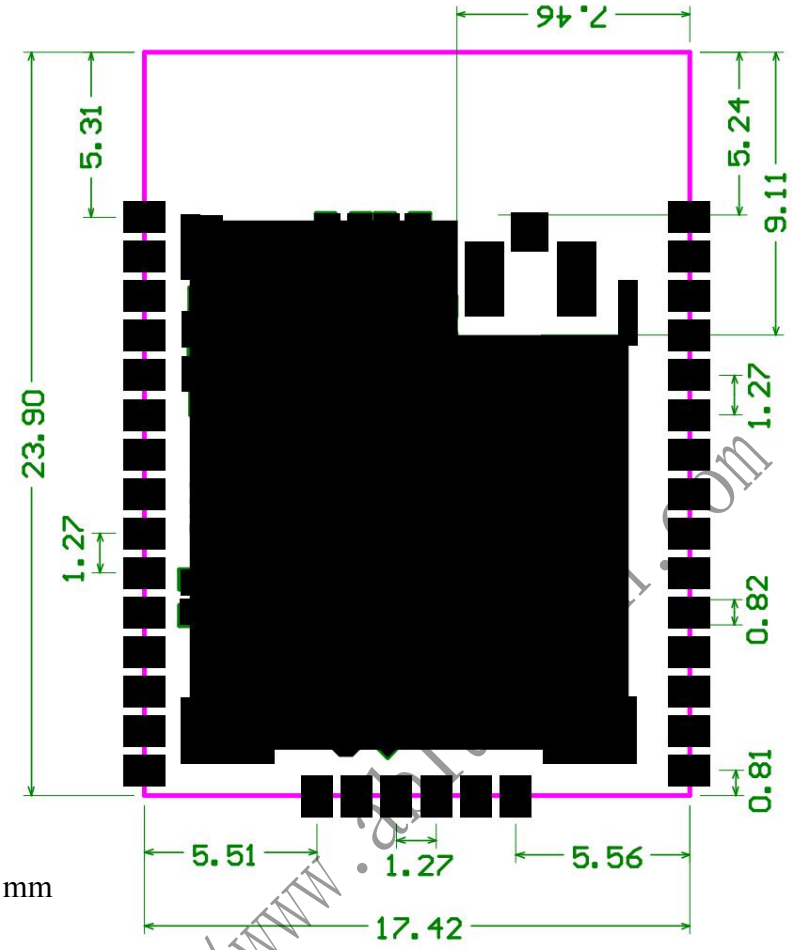
The module integrated antenna requires a suitable ground plane to radiate effectively. The area under and extending out from the antenna portion of the module should be kept clear of copper and other metal. The module should be placed at the edge of the PCB with the antenna edge facing out. Reducing the ground plane will reduce the effective radiated power. Please add as more as possible via holes on the mother board near the GND pin of module, this will be good for the RF performance of system board.



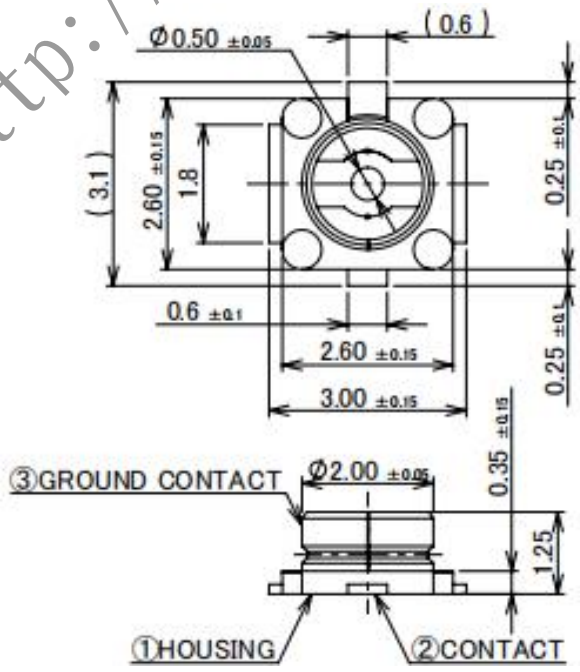
PCB Footprint （Top View）:



PTR9813+ IPX：



Unit: mm



Unit: mm

Radio Specifications:

Parameter	Min.	Typ.	Max.	Unit
Frequency Range	2402		2480	MHz
Maximum Output Power		+8		dBm
Rx Sensitivity Level, BLE1 Mbps		-96		dBm
Rx Sensitivity Level, BLE Long Range 125 kbps		-103		dBm
Data Rate on air	125		2000	kbps
Operating Temperature Range	-40	25	85	°C

Radio current consumption (transmitter):

Parameter	Min.	Typ.	Max.	Unit
TX only current (DC/DC, 3 V) PRF = +8 dBm		14.2		mA
TX only current (DC/DC, 3 V) PRF = +4 dBm		9.6		mA
TX only current (DC/DC, 3 V) PRF = +0 dBm		4.9		mA
TX only current (DC/DC, 3 V) PRF = -4 dBm		3.8		mA
TX only current (DC/DC, 3 V) PRF = -8 dBm		3.4		mA
TX only current (DC/DC, 3 V) PRF = -20 dBm		2.7		mA

Radio current consumption (Receiver):

Parameter	Min.	Typ.	Max.	Unit
RX only current (DC/DC, 3 V) 1 Mbps BLE		4.6		mA
RX only current (DC/DC, 3 V) 2 Mbps BLE		5.2		mA

Operating Conditions:

Parameter	Min.	Typ.	Max.	Unit
Supply voltages				
VDD	1.7	3.0	+3.6	V
VDDH	2.5	3.7	+5.5	V
VBUS	4.35	5	+5.5	V
Operating Temperature Range	-40	25	85	°C

Absolute Maximum Ratings:

Parameter	Min.	Max.	Unit
Supply voltages			
VDD	-0.3	+3.9	V
VDDH	-0.3	+5.8	V
VBUS	-0.3	+5.8	V
VSS	0	0	V
I/O pin voltage			

Voltage on GPIO pins ($V_{CC} \leq 3.6V$)	-0.3	VDD + 0.3	
Voltage on GPIO pins ($V_{CC} > 3.6V$)	-0.3	+3.9	
NFC antenna pin current		80	mA
RF input level		10	dBm
Environmental			
ESD Human Body Model		2	KV
ESD Human Body Model Class		3A	
ESD Charged Device Model		500	V
Storage temperature	-40	125	℃
Flash memory Endurance		10000	Write/erase cycles

Note: Exceeding one or more of the limiting values may cause permanent damage to the module.

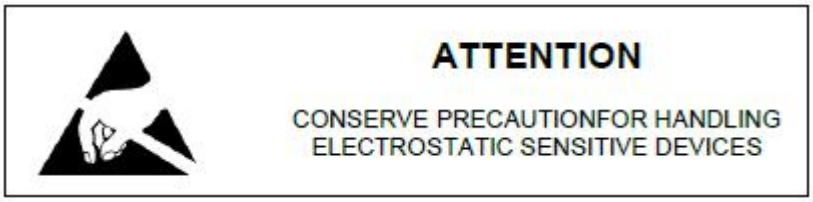
Notes and Cautions:

Design Notes

(1) It is critical to following the recommendations of this document to ensure the module meets the specifications.
(2) Power supply must be free of AC ripple voltage. If such noise is present, it is critical to provide proper filtering and decoupling.
(3) The module should not be stressed mechanically after installation.
(4) Exposing the module to significant temperatures will result in degradation and decreased lifetime.
(5) Keep module away from other high frequency devices which may interfere with operation such as other transmitters and devices generating high frequencies.
(6) Avoid static electricity, ESD and high voltage as these may damage the module.

Handling and Storage

(1) Keep module away from other high frequency devices which may interfere with operation such as other transmitters and devices generating high frequencies.
(2) Do not expose the module to the following conditions: Corrosive gasses such as Cl2, H2S, NH3, SO2, or NOX Extreme humidity or salty air Prolonged exposure to direct Sunlight Temperatures beyond those specified for storage.
(3) Do not apply mechanical stress.
(4) Do not drop or shock the module.
(5) Avoid static electricity, ESD and high voltage as these may damage the module.

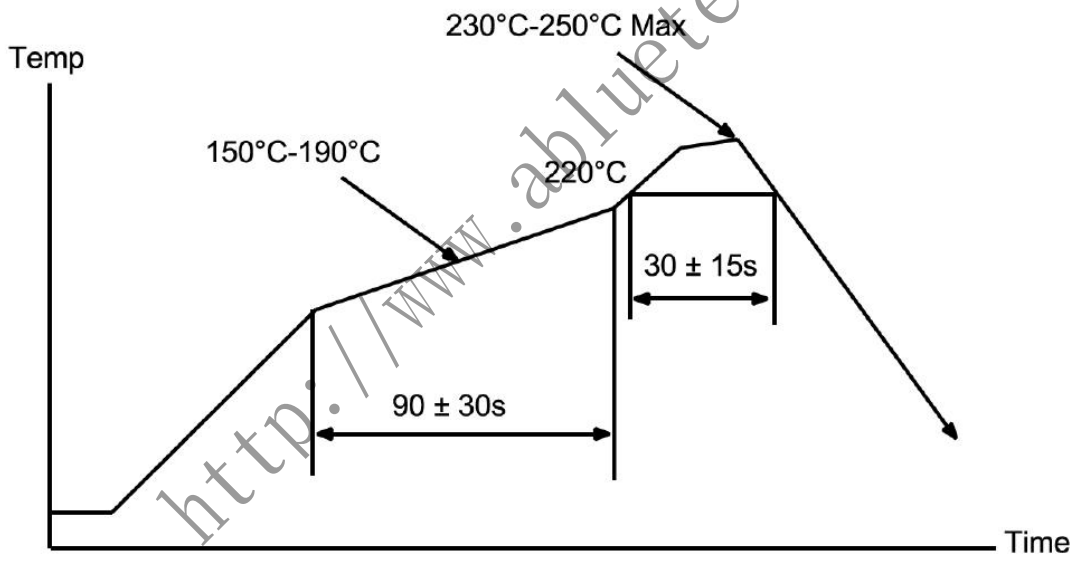


Moisture Sensitivity

All plastic packages absorb moisture. During typical solder reflow operations when SMDs are mounted onto a PCB, the entire PCB and device population are exposed to a rapid change in ambient temperature. Any absorbed moisture is quickly turned into superheated steam. This sudden change in vapor pressure can cause the package to swell. If the pressure exerted exceeds the flexural strength of the plastic mold compound, then it is possible to crack the package. Even if the package does not crack, interfacial delamination can occur.

Since the device package is sensitive to moisture absorption, it is recommended to bake the product before assembly.

Solder Reflow Temperature-Time Profile



Life Support Applications

Products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Customers using or selling these products for use in such applications do so at their own risk.

Additional Customization

We provide extensive customization, design and manufacturing services to ensure the perfect fit for your product. Our wide selection of modules allows developers to create any number of products. Should you need more information and assistance in integrating this module or developing your product, please contact us.

- Custom Hardware design including Modules, RF and Antenna Design

- Bluetooth Low Energy and Firmware Development
- Mobile Apps for iOS and Android
- Cloud Platform

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Ordering Information:

Part Number	Description
PTR9813	Bluetooth 5.2 System on Module，On board PCB antenna
PTR9813+	Bluetooth 5.2 System on Module，use with Ext. IPX Antenna
XANT-IPX-10	2.4GHz FPC Antenna with IPX connector，2dB gain
XANT-SMA-10	2.4GHz Omni Antenna with SMA connector, 3dB gain
XIPX-SMA-10	IPX to SMA Converter RF cable， use for IPX type connector of RF module to SMA type Antenna.
PTR9813-EVB	Evaluation boards for module，with key, LED, I/O extend